



“Enhanced autofocus algorithm using robust focus measure”

Topic :	Enhanced autofocus algorithm using robust focus measure for students of MCA I, III and V by Dr. Yogendera Kumar, PhD IIT Roorkee, currently teaching at School of Electrical Engineering, Korea University, South Korea as Assistant Prof.
Date of event :	August 6, 2012
Aim of the Event :	The session aimed at giving insight to students into the research areas and current advancements in VLSI design and “system on chip” (SoC) concept.
Description of the event :	<p>The session started with a brief overview of the evolution of silicon technology by the speaker. It included a description about how the hardware design and fabrication advanced from large systems to compact systems. This has been achieved by integrating several heterogeneous components on a single chip.</p> <p>The speaker then went on to explain how cross functional areas of engineering, science, and technology came together to design the system, and he elaborated on the steps of SoC design. The applications of SoC are widespread across various domains such as implantable heart pacemaker and camera for blind</p>

	<p>which the speaker had worked on during his research.</p> <p>He explained the entire design process of a digital camera as an example, including its auto focusing algorithm for focus measure determination.</p> <p>He also discussed about the future applications of SoC which aim at ambient intelligence that involves getting all utility devices embedded onto a single chip. A Q&A after-session directed the students on how to learn and grow in the field of VLSI design.</p>
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